

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination ERK ET AL.	
		Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

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*	B	US-6,361,403	03-2002	Kuramochi et al.	451/41
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	M	US-			

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Silicon Processing for the VLSI Era, 2002, Lattice Press, Vol. 4, p. 336.
	V	Callister, Materials Science and Engineering, 1997, John Wiley & Sons, 4th ed., pp. 237-38.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.